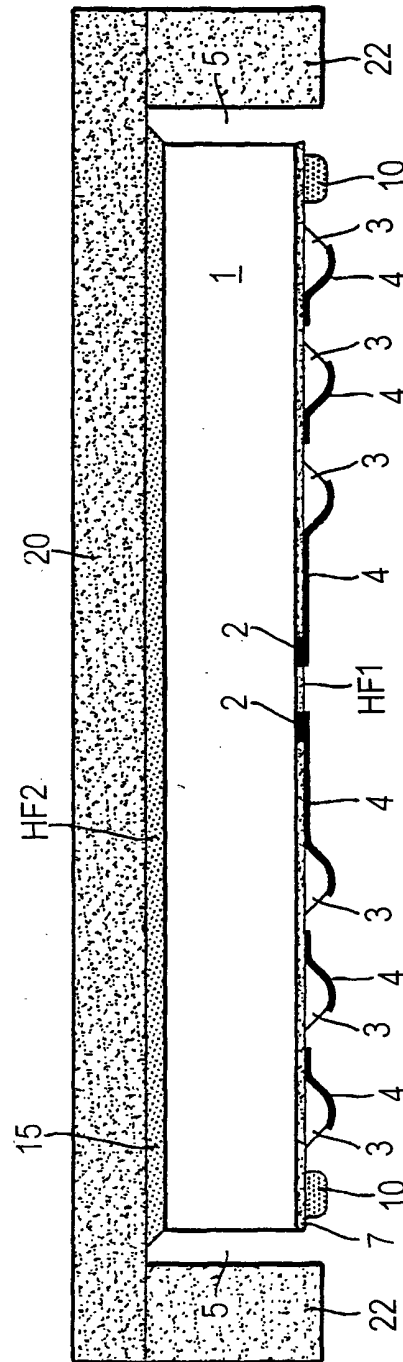


FIG. 1



Applicant(s): Harry Hedler

METHOD FOR CONNECTING AN INTEGRATED CIRCUIT TO  
A SUBSTRATE AND CORRESPONDING CIRCUIT  
ARRANGEMENT

FIG. 2A

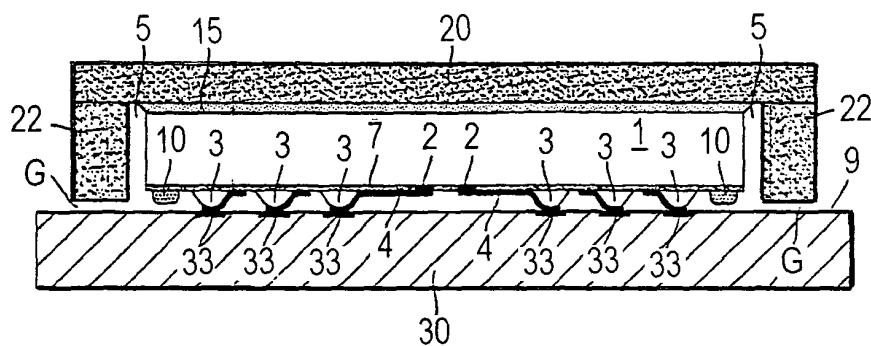


FIG. 2B

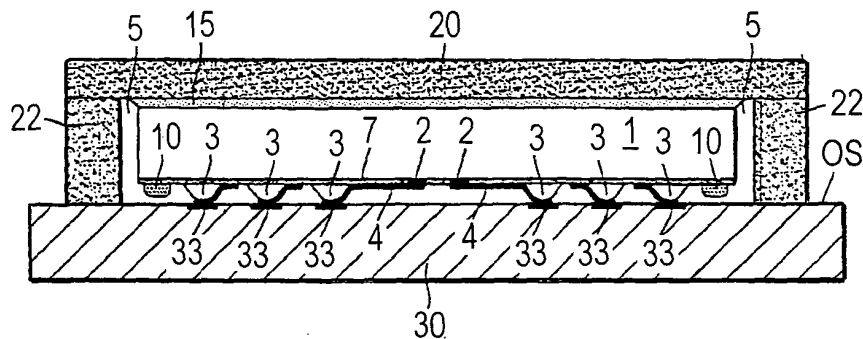


FIG. 2C

